

## Description

### JMP N-channel Enhancement Mode Power MOSFET

#### Features

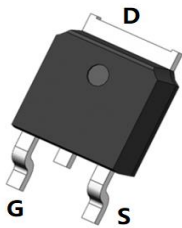
- 500V, 5A  
 $R_{DS(ON)} < 1.81\Omega @ V_{GS} = 10V$
- Fast Switching
- Improved dv/dt Capability

#### Applications

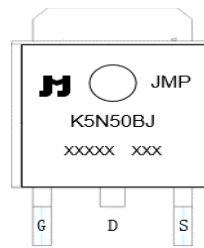
- Load Switch
- PWM Application
- Power Management



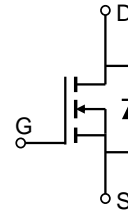
*100% UIS TESTED!*  
*100% ΔVds TESTED!*



TO-252-3L(DPAK) Top



Marking and Pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
JMPK5N50BJ	JMPK5N50BJ	TAPING	TO-252-3L	13"	2500	25000

## Absolute Maximum Ratings (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	500	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 30$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	5
		$T_C = 100^\circ\text{C}$	3
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	20	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	137	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	83
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(3)</sup>	33	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.5	
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	°C



## Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	500	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 500V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±30V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	3	4	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(4)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.5A	-	1.40	1.81	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz	-	615	-	pF
C <sub>oss</sub>	Output Capacitance		-	67	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	10	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 250V, I <sub>D</sub> = 2A	-	14	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	3.3	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	4	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 240V I <sub>D</sub> = 2A, R <sub>GEN</sub> = 24Ω	-	12	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	17	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	45	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	25	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	20	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I <sub>F</sub> = 5A, di/dt = 100A/us	-	340	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	2.9	-	μC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. E<sub>AS</sub> condition: Starting T<sub>J</sub>=25C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, R<sub>G</sub>=25ohm, L=10mH, I<sub>AS</sub>=5.3A
  3. RθJA is measured with the device mounted on a minimum recommended pad of 2oz copper FR4 PCB
  4. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

## Typical Performance Characteristics

Figure 1: Output Characteristics

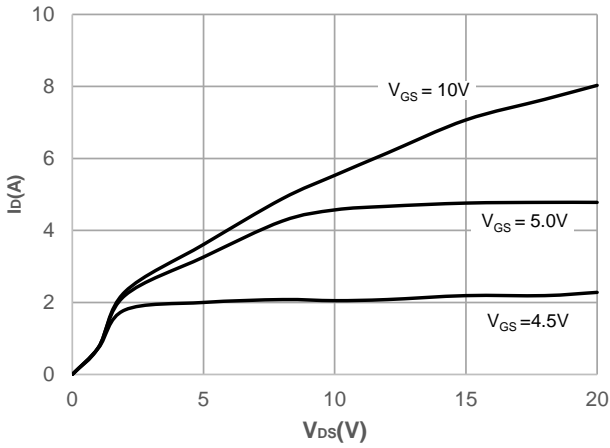


Figure 2: Typical Transfer Characteristics

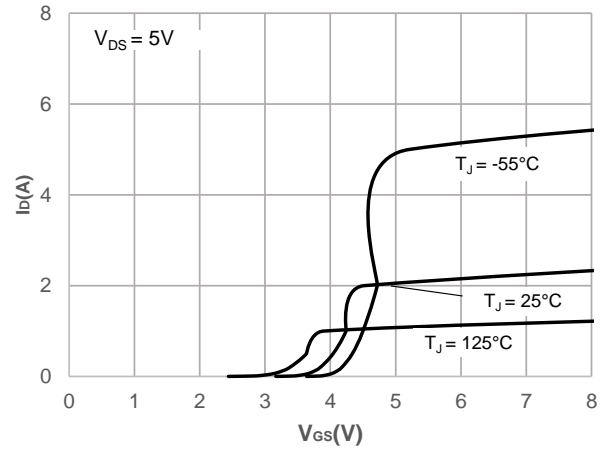


Figure 3: On-resistance vs. Drain Current

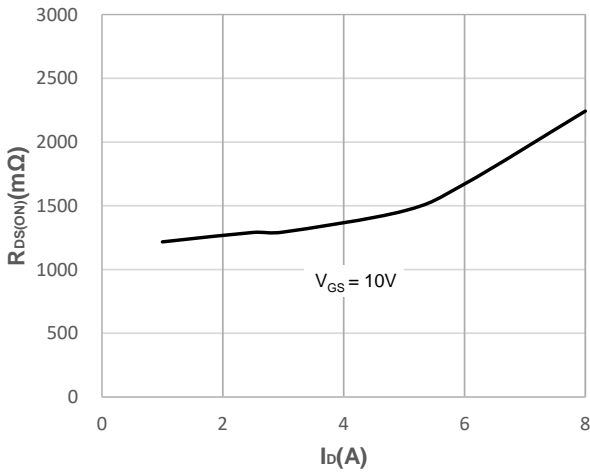


Figure 4: Body Diode Characteristics

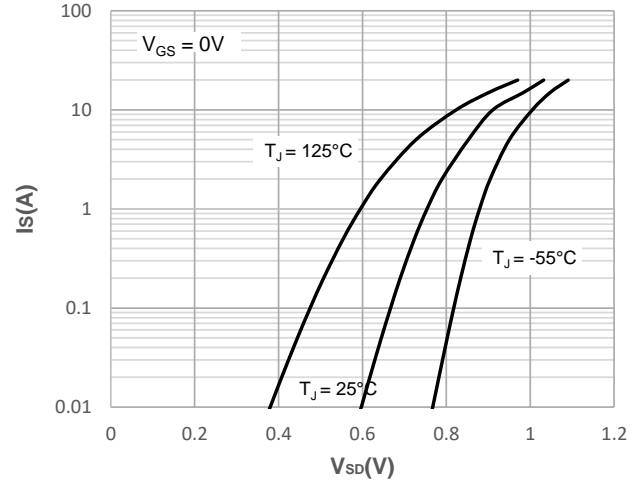


Figure 5: Gate Charge Characteristics

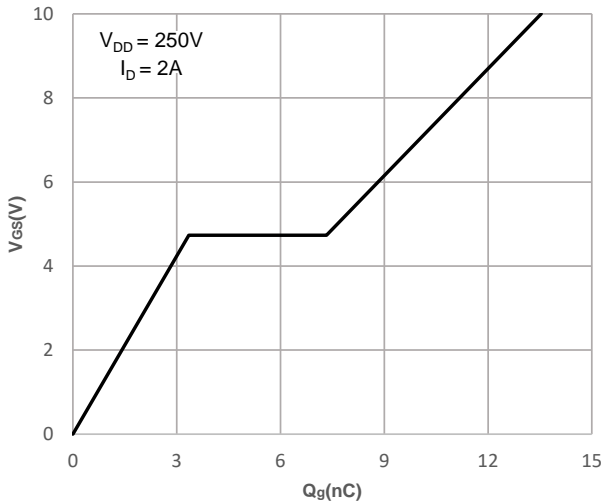
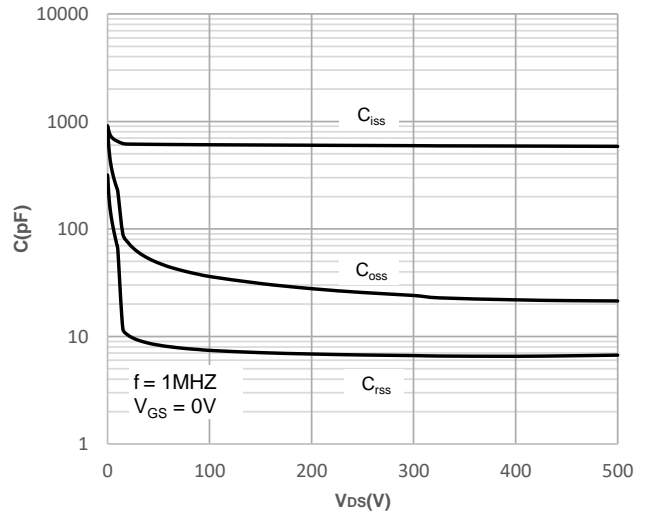


Figure 6: Capacitance Characteristics



## Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

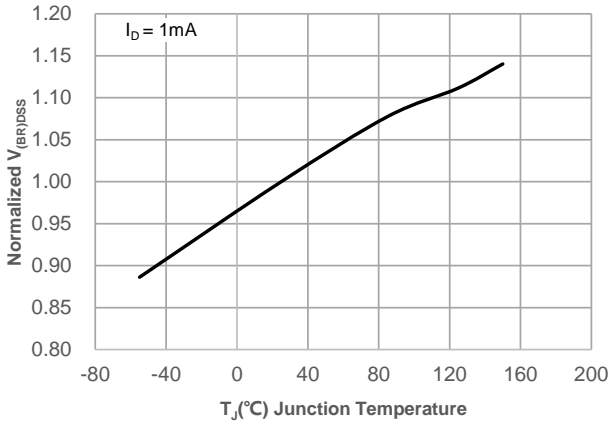


Figure 8: Normalized on Resistance vs. Junction Temperature

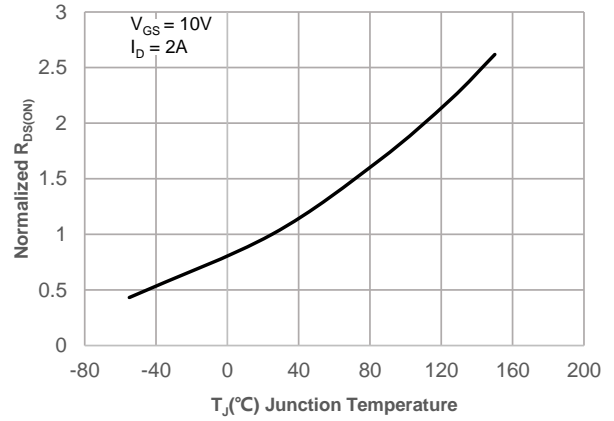


Figure 9: Maximum Safe Operating Area

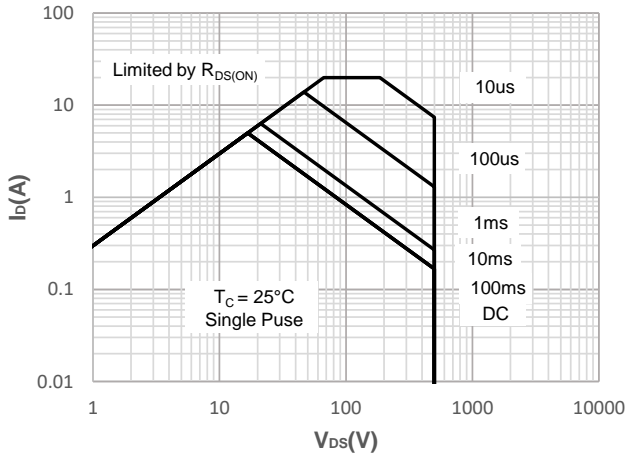


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

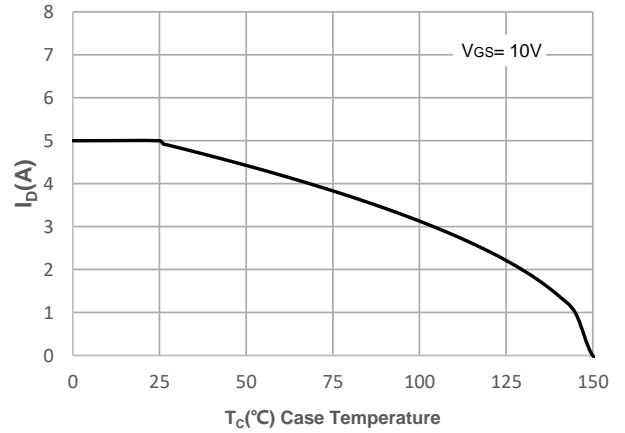


Figure 11: Normalized Maximum Transient Thermal Impedance

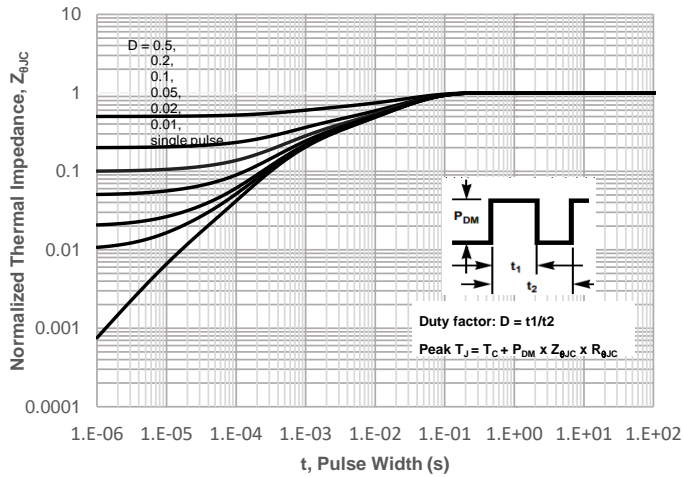
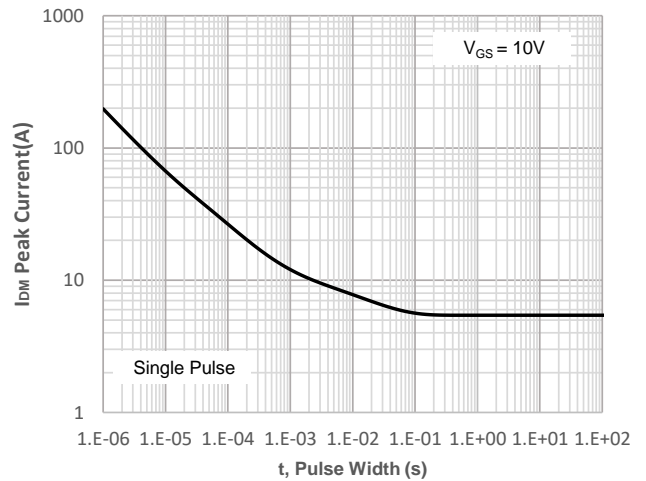


Figure 12: Peak Current Capacity



## Test Circuit

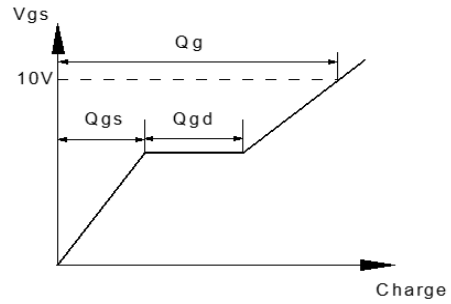
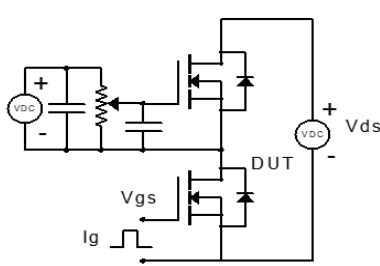


Figure 1: Gate Charge Test Circuit & Waveform

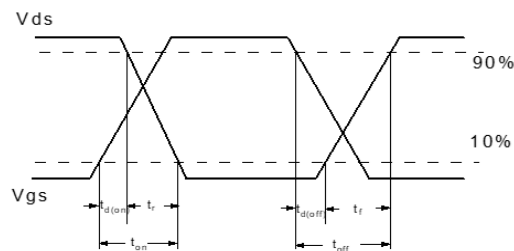
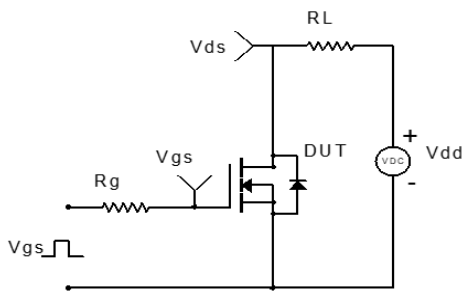


Figure 2: Resistive Switching Test Circuit & Waveform

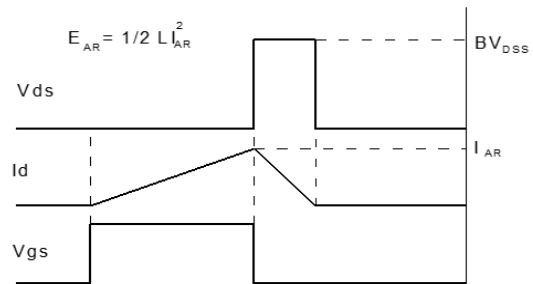
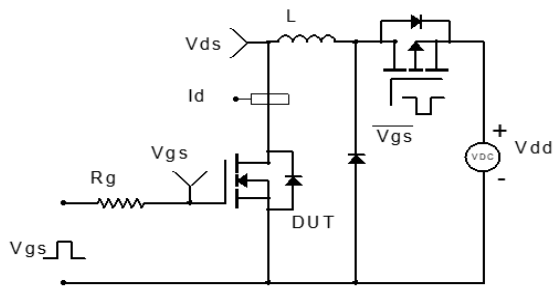


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

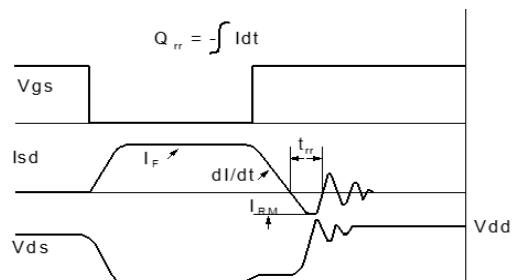
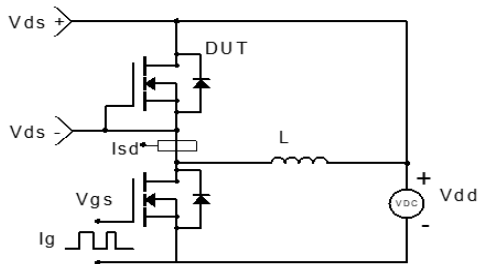
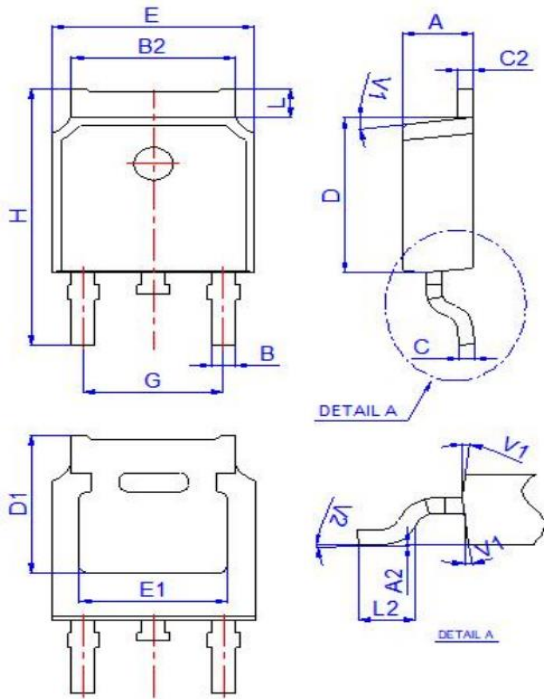


Figure 4: Diode Recovery Test Circuit & Waveform

## Package Mechanical Data(TO-252-3L)



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2		0°	6°		0°	6°

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